

Department of Electronics and Communication Engineering



IEEE Blended Learning Program



on

Fundamentals of System Verilog

An Inaugural function on **IEEE Blended Learning Program on VLSI “ Fundamentals of System Verilog”** was conducted on **11th August 2016** for ECE department students. Totally 32 students has registered for this course out of which 29 students have attended the inaugural function. The chief guest **Mr.Damodara M S, Product Manager, Global IEEE, Bangalore**, delivered the inaugural address for this function.

Inaugural Address key points:

1. Importance of design engineer in this global world.
2. Job opportunities in the field of VLSI design.
3. Outline of the IEEE blended learning program and its different courses.
4. Discussion on difference between Verilog and the System Verilog and its importance.
5. Demo on How to register IEEE Course and How to use course content through online.

