

Registration:

Manuscript should be prepared strictly as per IEEE format in MS-Word, not exceeding 6 pages and submitted online to the mail-id atee2017@kamarajengg.edu.in

Registration Fee:

Academicians / PG Students / Full Time Research Scholars: **Rs.500/-**

The registration fee includes the conference kit, proceedings, high tea and working lunch. Participants are requested to send the duly filled Registration form attested by Head of the Institution / Head of the Department along with the registration fee in the form of DD drawn in favor of "**Kamaraj College of Engineering & Technology**", payable at **Virudhunagar** on or before 24th March 2017.

The fee does not cover the accommodation charges and TA/DA. Delegates will have to make their own arrangements for accommodation. However, on prior request, for outstation delegates, accommodation could be provided in college premises at moderate charges.

Important Dates:

Last date for Full Paper Submission : 17.03.2017
Intimation of Paper Acceptance : 20.03.2017
Last date for Registration : 24.03.2017
Conference Date : 31.03.2017

Location:

Kamaraj College of Engineering & Technology is located in Virudhunagar district. It is nearer to many places namely Madurai, Aruppukottai, Sivakasi and Sattur. The college campus is at a distance of 40 kms from Madurai and 10 kms from Virudhunagar.

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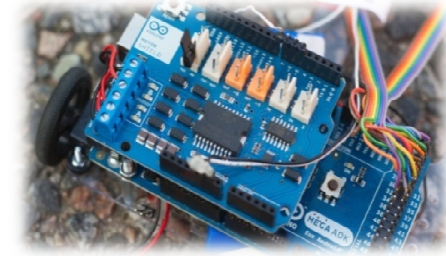
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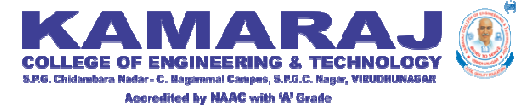
NATIONAL LEVEL CONFERENCE On ADVANCED TECHNOLOGIES IN ELECTRICAL ENGINEERING (ATEE – 2017)

31st March, 2017



Organized by
**DEPARTMENT OF ELECTRICAL AND
ELECTRONICS ENGINEERING**

(Reaccredited by NBA, New Delhi)



(Accredited by NAAC-'A' grade)
Virudhunagar - 626 001
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About the Institution

Kamaraj College of Engineering & Technology, a self-financing institution, offers Quality Technical Education over a decade. This institution has been approved by AICTE, New Delhi and affiliated to Anna University, Chennai. Also, this institution has been accredited by NAAC, Bangalore with "A" grade. The college offers ten UG and eight PG programmes in Engineering and Technology.

About the Department

Mission To impart highly innovative and technical knowledge in the field of Electrical and Electronics Engineering to the urban and unreachable rural student folks through "Total Quality Education".

Vision To make the Department of Electrical and Electronics Engineering of this Institution a unique of its kind in the field of Research and Development activities in this part of the world.

Quality Policy The Department of Electrical & Electronics Engineering is committed to impart Quality Technical Education imbued with proficiency, human values and continual improvement.

The Department of Electrical and Electronics Engineering was started in 2002. The Department has a PG course in Power Systems Engineering and also recognized as Research Center by Anna University, Chennai. The Department has highly qualified and experienced faculty members in different fields of specialization. The Department is nourished with latest equipments & computing facilities supported by softwares such as ETAP, PSCAD, CYME etc.,

About the Conference

The conference keeps its eye on the advancements in the field of Electrical Engineering. The mission of this conference is to make students and professionals to enrich their knowledge towards recent trends and developments in the field of Electrical Engineering. This conference will facilitate the participants to share knowledge in their own expertise and also share the experiences of professionals from reputed organizations.

Call for papers

Original unpublished papers are invited in the following areas but not limited to

- Power System Dynamics and Stability
- Modern Power Systems
- Electrical Drives and Control
- Modeling and Simulation
- Energy Conservation and Management
- Distributed Computing
- Power Quality
- Embedded Systems
- HVDC & FACTS
- Control & Automation
- Nano-electronics
- Distributed Generation
- Smart Grid
- Renewable Energy
- Energy Storage Technologies
- DG and Microgrid
- Power Electronics and Drives
- Soft computing Techniques

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REGISTRATION FORM

NATIONAL LEVEL CONFERENCE ADVANCED TECHNOLOGIES IN ELECTRICAL ENGINEERING (ATEE – 2017) 31st March, 2017

1. Name:
2. Designation:
3. Institution / Organization:

4. Address for Communication

5. Email ID:
6. Phone :
7. Accommodation Required : YES/NO
8. Participant Category :
Faculty/Research Scholar/PG Student
9. Payment Particulars:
DD. No & Date :
Amount :
Date :
Name of the Bank :

Date: _____ Signature of the Applicant

BONAFIDE CERTIFICATE

Mr./Ms./Dr. _____ of the _____ is
Permitted to attend the National Conference organized
by Department of EEE, Kamaraj College of Engineering
& Technology, Virudhunagar on 29th March 2017.

Date: _____ Signature of the Head of the
Department/Institution with Seal